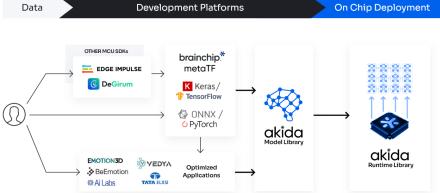
The Akida FPGA Platform is a hardware product for loading Akida IP configurations and neural models to evaluate the model performance and execution on BrainChip Akida IP. It provides system and chip designers with a pre-configured environment for demonstration, emulation, validation, and system integration. The platform showcases BrainChip's Akida Al neural processing acceleration, which is scalable, configurable, and programmable to support CNN and Temporal Event-Based Neural Network models (TENNs).

The Akida FPGA Platform provides access to run various Akida IP configurations loaded into FPGA, to verify model performance and IP configuration before moving to silicon. Delivered through a secure cloud interface, this turnkey solution allows users to immediately test the performance and capabilities of the Akida IP without any local hardware or software setup.

Features

- Preconfigured Akida IP core designs
- Broad Al Model Compatibility
- Pre-Configured Model Examples to test Akida
- Configurable On-Chip Memory
- Event-Based Neuromorphic Engine
- MetaTF Software Environment





Applications



Consumer products (Wearables)



Smart Home / Business



Retail



Industrial IoT



Defense



Automotive



Key Benefits

- ✓ Verify model performance on hardware
- Quantify latency and power before moving to silicon
- ✓ Execution of CNN, and TENNs
- ✓ Industry Standard Development Environment
- ✓ Low Host CPU overhead
- Sparsity efficient design with event-based data flow computations